

ACKNOWLEDGEMENT RECEIPT

Electronic Version 1.1

Stylesheet Version v1.1.1

Title of Invention	Avoiding Excessive Cross-terminal Voltages of Low Voltage Transistors Due to Undesirable Supply-Sequencing in Environments With Higher Supply Voltages
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Submission Type : Utility Patent Filing

Application Number:

10/711532



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First Named Applicant:

Bhupendra SHARMA

Attorney Docket Number:

TI-38242

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us-fee-sheet	us-fee-sheet.dif	11069	2004-09-24
us-request	EFStxn-usreq.xml	3163	2004-09-24
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us-request	us-request.xls	33904	2004-09-24
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application-body	application-body.dif	49498	2004-09-24
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application-body	sopub.ent	6621	2004-09-24
application-body	sotech.ent	5268	2004-09-24
application-body	mathml2.dtd	54588	2004-09-24
application-body	mathml2-qname-1.mod	13225	2004-09-24
application-body	rmlalias.ent	38209	2004-09-24
application-body	mmlextra.ent	7901	2004-09-24
application-body	soextbx.dtd	12870	2004-09-24
application-body	us-application-body.xls	82504	2004-09-24
application-body	wipo.ent	4956	2004-09-24
application-body-pdf-wrap	ABXspectxn108-pdf-wrap.xml	757	2004-09-24
abstract-pdf	ABXspectxn108-abst.pdf	9698	2004-09-24
claims-pdf	ABXspectxn108-claims.pdf	30373	2004-09-24
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us-declaration	dec1txn108.tif	64416	2004-09-24
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